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troobureau B.V., Prof. Holstlaan 6, NL-5656 AA Eind-
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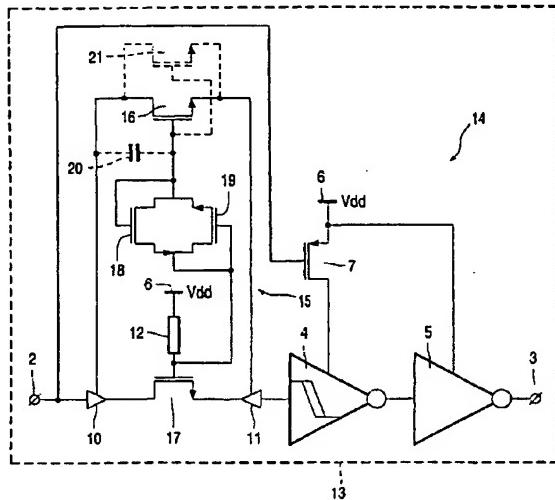
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KLIJKE PHILIPS ELECTRONICS N.V. [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventor; and

(75) Inventor/Applicant (for US only): MANDAL, Pradip
[IN/NL]; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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(54) Title: A VOLTAGE LIMITING SEMICONDUCTOR PASS GATE CIRCUIT



(57) Abstract: A voltage limiting semiconductor pass gate circuit (15), comprises a first transistor (16), operatively connected to an input node (10) and an output node (11) of the pass gate circuit (15), and a second transistor (17), operatively connected between the input node (10) and the output node (11). The second transistor (17) has a control electrode biased to a supply voltage (6), and the first transistor (16) has a control electrode which connects by two back-to-back connected diode elements (18, 19) to the control electrode of the second transistor (17). The pass gate circuit (15) is typically applied in input I/O cells (14) of semiconductor integrated circuits (13).

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INTERNATIONAL SEARCH REPORT

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IPC 7 H03K17/06

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

PAJ, EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP 60 177714 A (HITACHI SEISAKUSHO KK) 11 September 1985 (1985-09-11) figure 1	1-3
Y	---	4-8
Y	US 6 271 703 B1 (WERT JOSEPH DOUGLAS) 7 August 2001 (2001-08-07) column 5, line 24-43; figure 4	4-8
A	-----	1-3



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Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Meulemans, B

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JP 60177714	A 11-09-1985	NONE	
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